



# SV COLLEGE OF ENGINEERING

(AUTONOMOUS)

Karakambadi Road, Tirupati-517507

R 20

## EXAMINATION BRANCH

### M.Tech II Semester II Internal Examinations Sep-2023 TIME TABLE

FN: 08.30 a.m. to 10.30 a.m

Date/Day	VLSI Design (VLSID )	Computer Science and Engineering (CSE)
14-09-2023 (Thursday)	CMOS Mixed Signal Design	Advanced Algorithms
15-09-2023 (Friday)	Physical Design Automation	Advanced Computer Architecture
16-09-2023 (Saturday)	MEMS System Design	Network security
20-09-2023 (Wednesday)	Low power VLSI Design	Human Computer Interaction
21-09-2023 (Thursday)	Entrepreneurship & Incubation	Enterprise Resource planning

*P. Rajitha*  
CONTROLLER OF EXAMINATIONS  
*PO*

*NAN*  
PRINCIPAL

#### Copy To:

1. Additional Controller of Examinations.
2. HOD (ECE & CSE) with a request to circulate among Staff and students and make arrangements to send all the question papers (soft copy & hard copy) to the Exam cell on or before 11-09-2023, 02.00 P.M.
3. All Notice Boards, T & P Cell, I/C Security, I/C Hostel, I/C Transport, I/C Canteen and AO for information.

04/09/2023